DEEPWAVE DIGITAL

End-to-End Signal Processing and Deep Learning (using Embedded GPUs)

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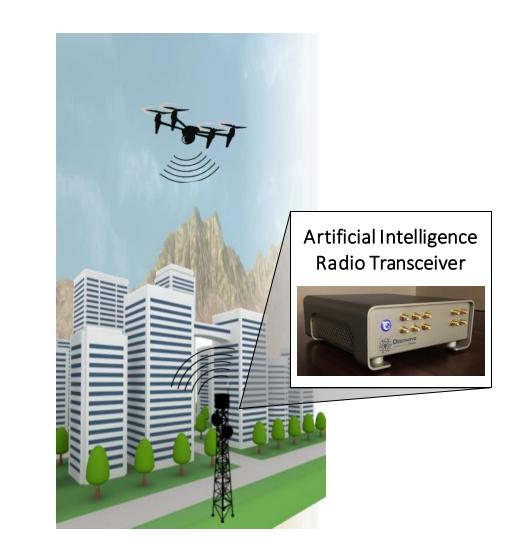
Deepwave Digital

Enabling the Incorporation of Deep Learning and Radio Frequency (RF) Systems

- <u>Full stack solutions</u> for deep learning and GPU enabled signal processing systems
 - Edge compute hardware
 - Custom Applications
 - Tight coupling of hardware and software for performance
 - Radio embedded with FPGA, CPU, GPU
 - GPU-based signal processing algorithms
 - Pruned neural networks for inference on edge RF systems

• <u>Testing and deployment platform</u> for customer developed applications

- AIR-T open platform for custom applications
- Streamlines development, testing, and deployment
- Many open source software tools



AI to Solve Complex Problems

Artificial Networks Using Deep Learning

Simple Example: Image Recognition



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Simple Example: Image Recognition



AI to Solve Complex Problems

Artificial Networks Using Deep Learning

Simple Example: Image Recognition



Deep Learning identifies intricate patterns that are too obscure and subtle to be implemented into a human-engineered algorithm

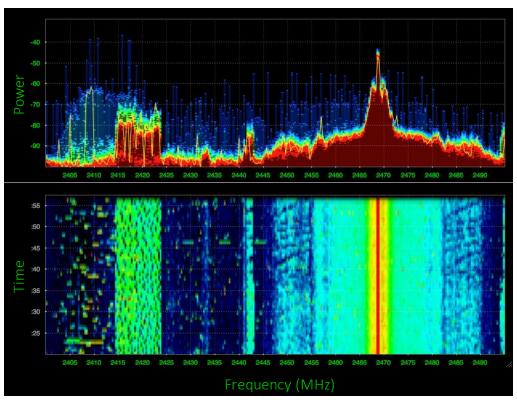
Al to Solve Complex Problems

Artificial Networks Using Deep Learning

Simple Example: Image Recognition



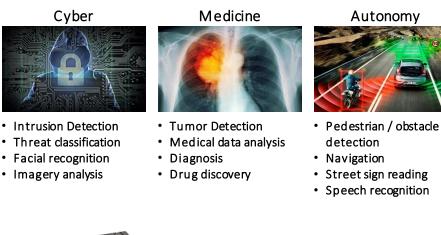
Congested Wireless Spectrum

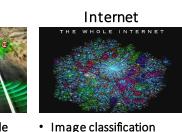


Deep Learning identifies intricate patterns that are too obscure and subtle to be implemented into a human-engineered algorithm

Deep Learning and Radio Frequency (RF) Systems

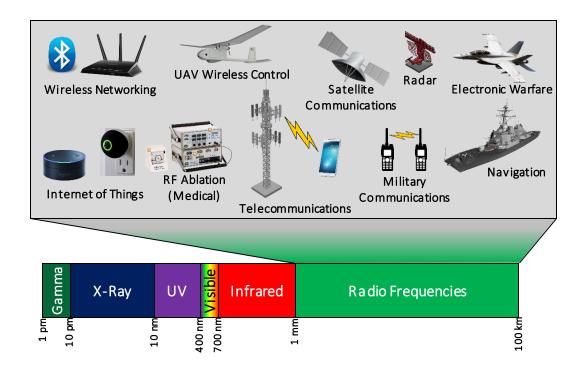
Deep Learning is Emerging





- Speech recognition
- Language translation
- Document / database searching

Radio Frequency Technology is Pervasive

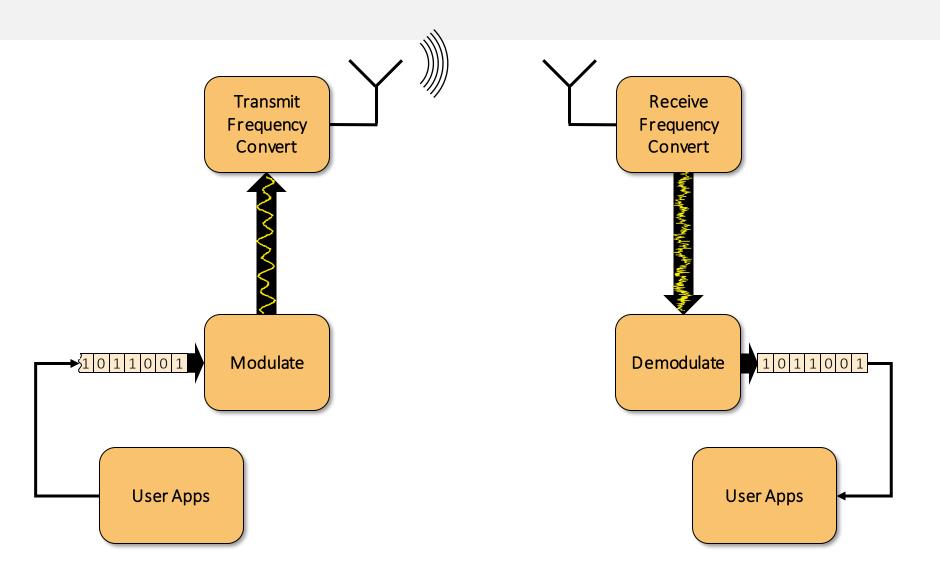


Deep learning technology enabled and accelerated by GPU processors - Only beginning to impact design and applications in wireless and radio frequency systems

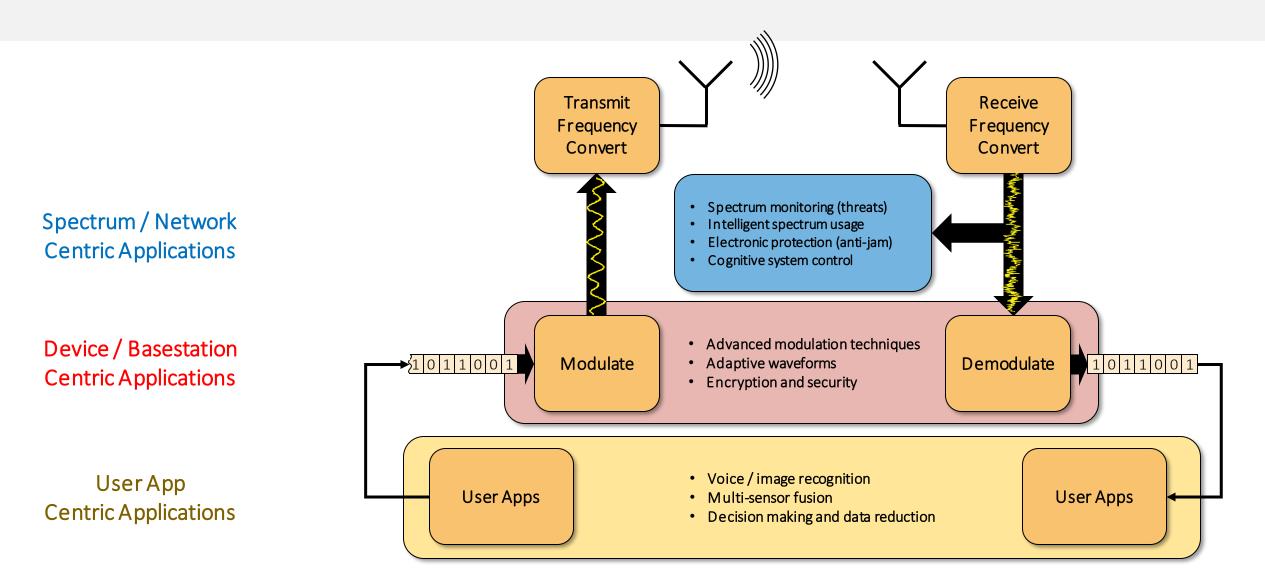


Enabled by low-cost, highly capable general purpose graphics processing units (GPUs)

Where to Use Deep Learning in RF Systems

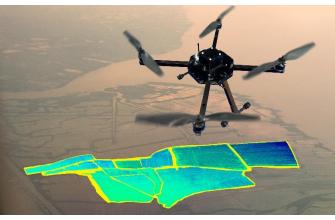


Where to Use Deep Learning in RF Systems



Deep Learning Comparison

Image and Video



- Multiple channels (RGB)
- x, y spatial dependence
- Temporal dependence (video)

Audio and Language



- Single channel
- Frequency, phase, amplitude
- Temporal dependence

RF Systems and Signals



- Multiple channels
- Frequency, phase, amplitude
- Temporal dependence
- Complex data (I/Q)
- Large Bandwidths
- Human engineered

Existing deep learning potentially adaptable to systems and signals

• Must contend with wideband signals and complex data types

Why Has Deep Learning in RF Not Been Addressed

Backhaul Bandwidth

- Insufficient bandwidth to upload to data center for processing
- Applications are latency sensitive

Edge Compute Resources

- Insufficient resources for AI/RF applications
- No RF agile AI / RF radio systems

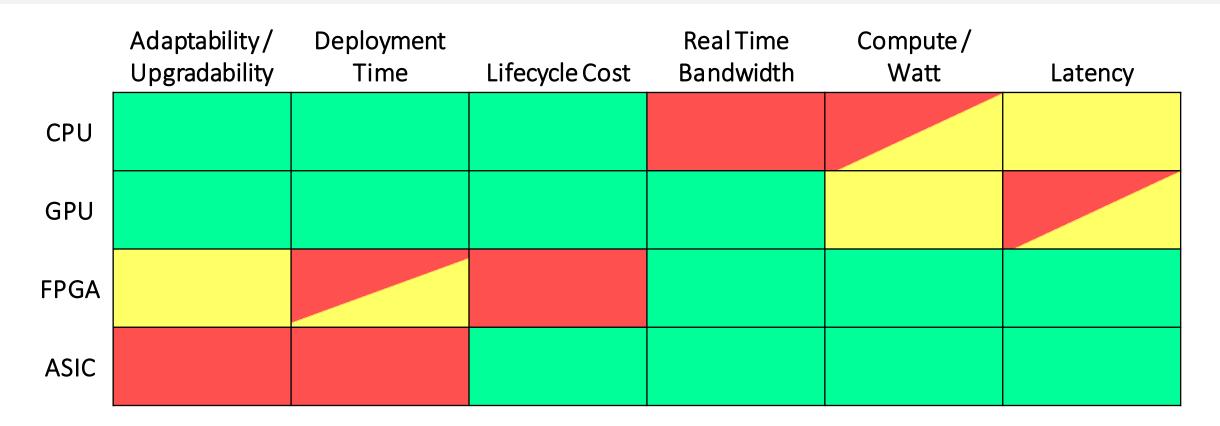
Disjointed software

- Complications of AI / signal processing software merger
- No existing unifying framework

Hardware for Deep Learning in RF Systems

	Trainii	ng	Inference					
	Pros	Cons	Pros	Cons				
CPU	Supported by ML FrameworksLower power consumption	Slower than GPUFewer software architectures	Adaptable architectureSoftware programmableMedium latency	 Low parallelism Limited real-time bandwidth Medium power requirements 				
GPU	 Supported by ML Frameworks Widely utilized Highly parallel / adaptable Good throughput vs power 	 Overall power consumption Requires highly parallel algorithms 	 Adaptable architecture High real-time bandwidth Software programmable 	Medium power requirementsNot well integrated into RFHigher latency				
FPGA	Not widely utilized, r (yet)	not well suited	High power efficiencyHigh real-time bandwidthLow latency	 Long development / upgrades Limited reprogrammability Requires special expertise 				
ASIC	Not widely utilized, r	not well suited	 Extremely power efficient High real-time bandwidth Highly reliable Low latency 	 Extremely expensive Long development time No reprogrammability Requires special expertise 				

Critical Performance Parameters for Deep Learning in RF Systems



GPU signal processing can provide wideband capability and software upgradability at lower cost and development time

- Must contend with increased latency (~2 microsecond)

Artificial Intelligence Radio Transceiver (AIR-T)

AIR-TPlatform

6.7 inch (17 cm)

Mini ITX Form Factor

Versatile embedded RF transceiver for cellular, communications, and defense applications

Embedded deep learning processor

System Specifications

2x2 MIMO Transceiver

- Tunable from 300 MHz to 6 GHz
- 125 MSPS (100 MHz bandwidth per channel)

• Digital Signal / Deep Learning Processors

- Xilinx FPGA
- NVIDIA Jetson TX2
 - 6 CPU cores
 - 256 Core GPU
 - Shared GPU/CPU memory (zero-copy)

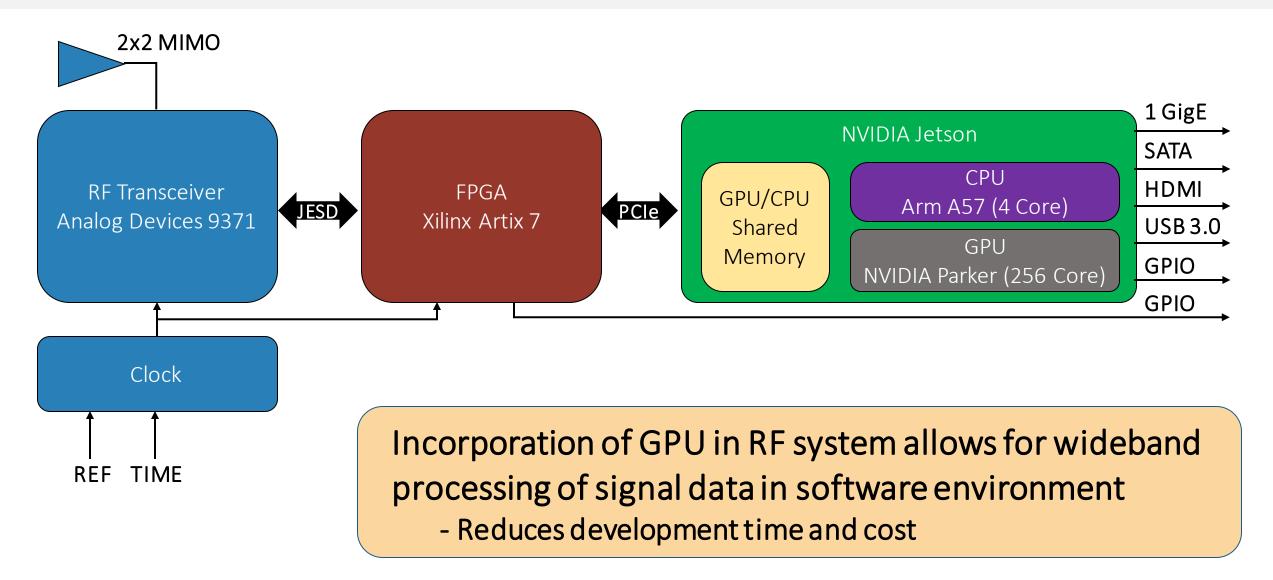
AirStack Software Suite

- Ubuntu Linux w/ Deepwave hardware drivers
- All common AI software frameworks supported
- Python or C++

The only software defined radio with built-in deep learning processors

Artificial Intelligence Radio Transceiver (AIR-T)

Block Diagram

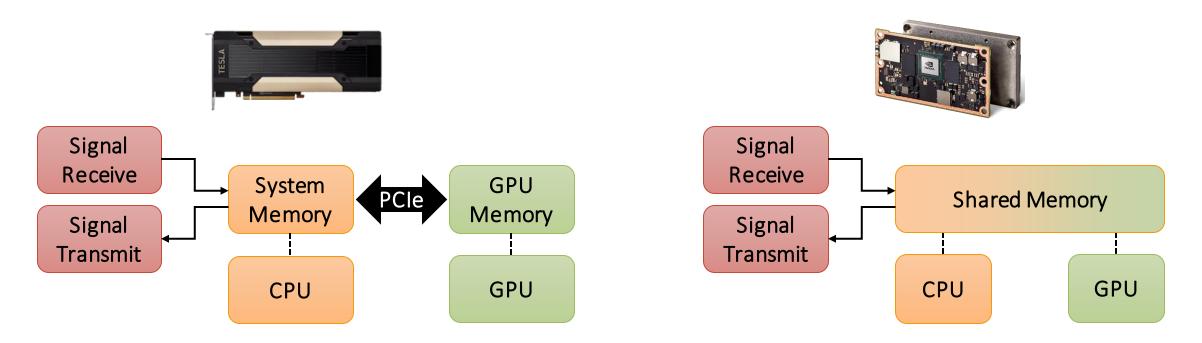


Shared Memory Architecture for Embedded GPUs

Reducing data copies and latency

Traditional GPU: PCIe

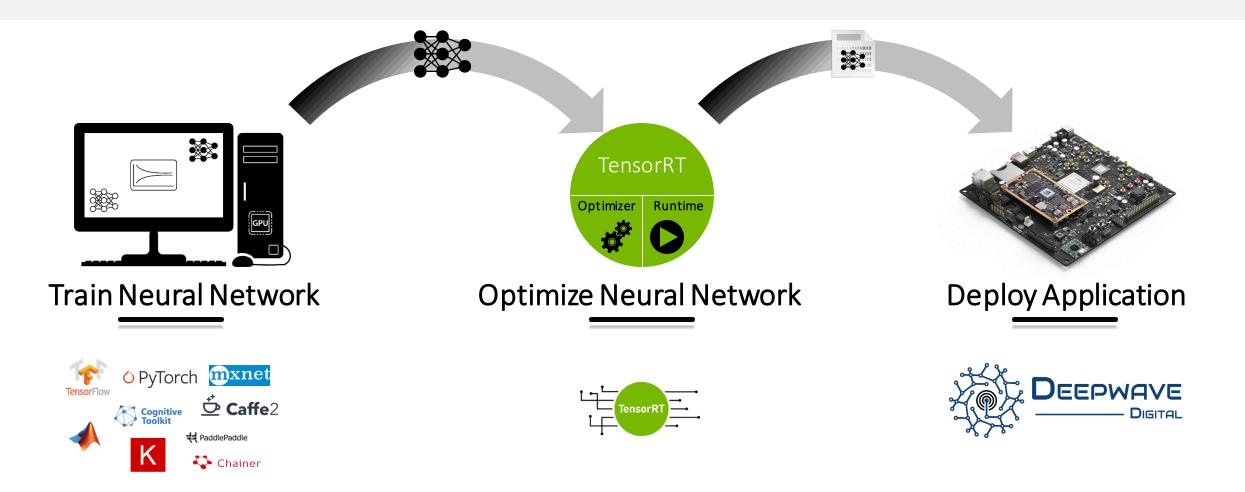
Jetson GPU: Embedded



Jetson Embedded GPUs eliminate extra data copy with GPU/CPU shared memory

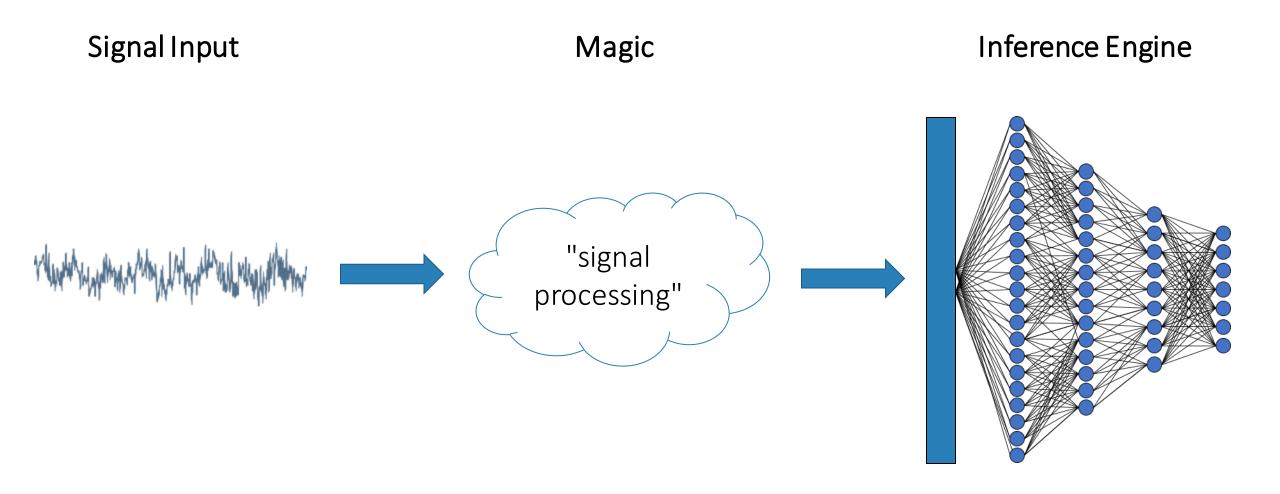
• Enables signal processing stream applications with latency driven requirements

Inference at the Edge with AirStack Software



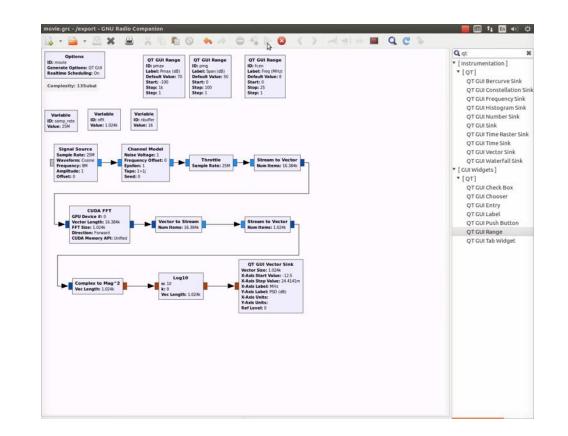
Streamlined workflow for deploying deep learning in software defined radio

Inference Pipeline for Signals



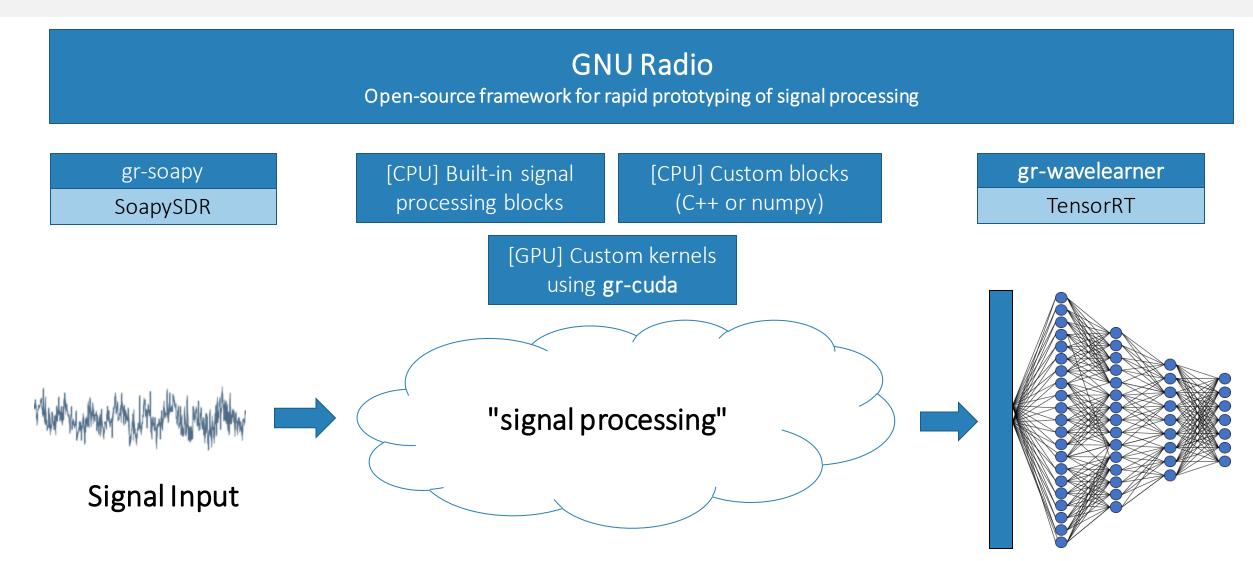
GNU Radio – Software Defined Radio (SDR) Framework

- Popular open source software defined radio (SDR) toolkit:
 - RF Hardware optional
 - Can run full software simulations
- Python API
 - C++ under the hood
- Easily create DSP algorithms
 - Custom user blocks
- Primarily uses CPU
 - Advanced parallel instructions
- Deepwave is integrating GPU support for both DSP and ML





Tying it Together in GNU Radio



Inference Engine

GPU Custom Signal Processing: GR-CUDA

Custom GPU Signal Processing with GNU Radio on the AIR-T

- Deepwave provides a simple example for wrapping a custom CUDA kernel with a GNU Radio block
 - Uses pyCUDA under the hood
 - Can place a series of operations into one block with a simple interface
 - Output can be routed to gr-wavelearner for inference
- Source code available on GitHub
- Full tutorial on Deepwave website

GR-CUDA GitHub: https://github.com/deepwavedigital/gr-cuda

[Audio] Variable Variable Variable [Boolean Operators] ID: divide by two ID: buffer size ID: samp rate ID: frequency Title: AIR-T Example Value: 31.25M Value: 2.4G Value: 16.384k [Byte Operators] Author: Deepwave Digital Generate Options: QT GUI Channelizers Channel Models Soapy Source [Coding] Device: device=SoapyAIRT **OT GUI Time Sink** Sampling Rate: 31.25M [Control Port] lumber of Points: 128 Ch0: Center Freq (Hz): 2.4G Sample Rate: 31.25M Ch0: Gain Value: 0 Debug Tools Autoscale: No Deprecated] Stream to Vector Jum Items: 16.384k [Digital Television] [Equalizers] [Error Coding] CUDA Kerne ▶ [FCD] GPU Device #: 0 Vector Length: 16.384k File Operators] Threads per Block: 128 Filters Fourier Analysis] GPU Acceleration Vector to Strea CUDA Kernel [GUI Widgets] << Welcome to GNU Radio Companion 3.7.9 >>> [Impairment Models] [Instrumentation] references file: /home/deepwave/.gnuradio/grc.conf lock paths: [Level Controllers /usr/local/share/gnuradio/grc/blocks [Math Operators] /usr/share/gnuradio/grc/blocks [Measurement Tools] /home/deepwave/.grc gnuradio [Message Tools] oading: "/usr/local/src/deepwave/gr-cuda/examples/divide-by-two.grc" [Misc] >>> Done Modulators Showing: "/usr/local/src/deepwave/gr-cuda/examples/divide-by-two.grc" [Networking Tools] [NOAA]

divide-by-two.grc - /usr/local/src/deepwave/gr-cuda/examples - GNU Radio Companion

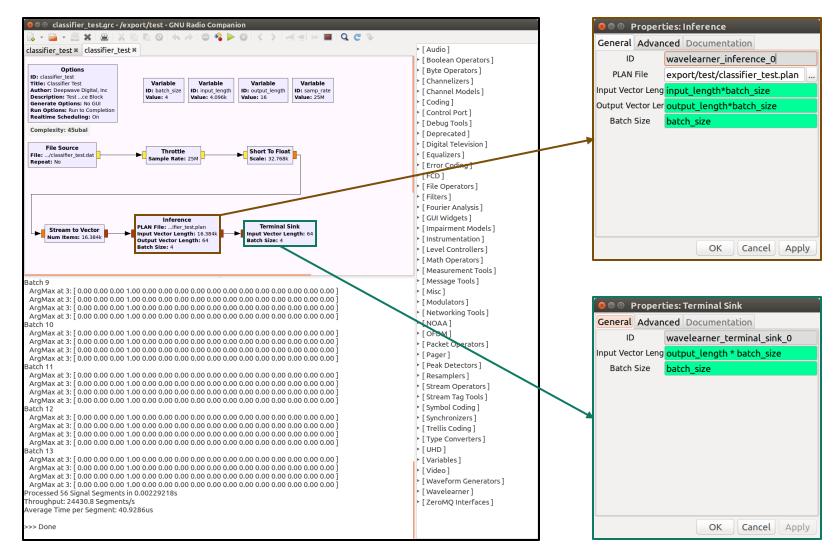
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<u>Deepwave Tutorial</u>: https://deepwavedigital.com/tutorials/custom-gpu-signal-processing-with-gnu-radio-on-the-air-t

GR-Wavelearner

Easily Incorporate Inference into Signal Processing

- Three blocks currently:
 - <u>Inference</u> wraps a serialized TensorRT neural network
 - <u>Terminal Sink</u> Python module for displaying classifier output
 - <u>FFT</u> cuFFT wrapper
- Open source module for GNU Radio
- C++ and Python API
- GPLv3 license
- README with instructions to get started quickly



What About Training?

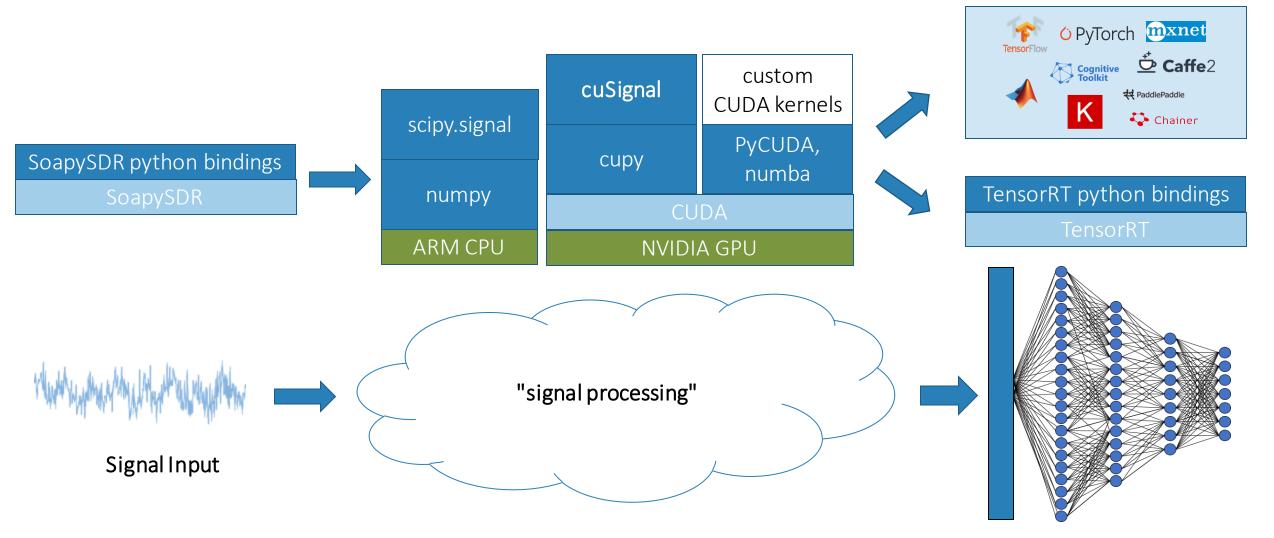




(some assembly required)

Where do I put my training wheels?

Python Stack for Training and Inference



Deep Learning Model

Example: A Simple Radio Interface

$\bigcirc \bigcirc \bigcirc \bigcirc$

import SoapySDR
from SoapySDR import SOAPY_SDR_RX, SOAPY_SDR_CF32

```
# Initialize the AIR-T radio using SoapySDR
sdr = SoapySDR.Device(dict(driver="SoapyAIRT")) # Create AIR-T instance
sdr.setSampleRate(SOAPY_SDR_RX, 0, 125e6) # Set sample rate
sdr.setGainMode(SOAPY_SDR_RX, 0, automatic=True) # Automatic gain control
sdr.setFrequency(SOAPY_SDR_RX, 0, 2.4e9) # Tune the radio
# Setup to receive data on channel 0 and turn the radio on
stream = sdr.setupStream(SOAPY_SDR_RX, SOAPY_SDR_CF32, [0])
sdr.activateStream(stream)
```

Example: A Simple Radio Interface

```
\bigcirc \bigcirc \bigcirc
import numpy
import numba.cuda
num samples = 2048
buffer = numba.cuda.mapped_array(num_samples, dtype=numpy.complex64)
while True:
    result = sdr.readStream(stream, [buffer], num_samples, timeoutUs=int(1e6))
    assert result.ret == num_samples
    # ... do stuff with samples in buffer
```

Data is transferred directly from the radio hardware into memory accessible by the GPU

Example: Power Estimation / Energy Detection

$\bigcirc \bigcirc \bigcirc \bigcirc$

import numpy

```
# buffer (length num_samples) contains data from the radio
input_array = numpy.asarray(buffer)
power_sum = numpy.sum(input_array.real ** 2 + input_array.imag ** 2)
average_power = power_sum / num_samples
```

 $\bigcirc \bigcirc \bigcirc \bigcirc$

import cupy

```
# buffer (length num_samples) contains data from the radio
input_array = cupy.asarray(buffer)
power_sum = cupy.sum(input_array.real ** 2 + input_array.imag ** 2)
average_power = float(power_sum) / num_samples
```

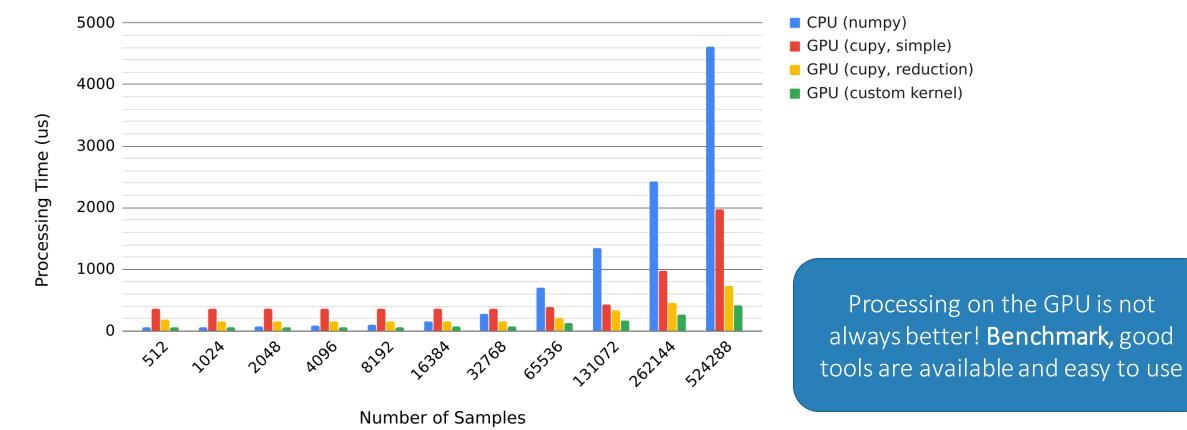
Processing data on the ARM CPU

Processing on the GPU

and reading back a single float

AIR-T: numpy vs. cupy vs. a custom kernel

"Average Signal Power": Computation Time on TX2



Deep Dive: Why did we see this performance?

- Annotate each signal processing function to capture a profile using *nvprof*
- cupy makes this simple, and you can analyze mixed CPU/GPU code just as easily
- Use pytest-benchmark to run each function many times...

```
000
import cupy.prof
def test_cupy(num_samples, benchmark):
    input = numba.cuda.mapped_array(num_samples, dtype=numpy.complex64)
    input[:] = numpy.complex64(1.0 + 0.0j)
    input_array = cupy.asarray(input)
    @cupy.prof.TimeRangeDecorator()
    def inner loop cupy():
        power_sum = cupy.sum(input_array.real ** 2 + input_array.imag ** 2)
        average_power = float(power_sum) / num_samples
    benchmark(inner_loop_numpy)
```

Deep Dive: Profiling Results

Profile of 16384 samples case, GPU (cupy) and GPU (custom kernel)

000													
==31551== Range "inner_loop_cupy"													
Type Time(5) Time	Calls	Avg	Min	Max	Name							
Range: 100.0	% 595.78ms	1002	594.59us	453.15us	10.181 ms	inner_loop_cupy							
GPU activities: 38.94	% 43.836ms	1002	43.748us	23.682us	96.553us	cupy_multiply							
38.1	% 42.883ms	1002	42.797us	27.747us	85.928us	cupy_conj							
16.3	% 18.446ms	1002	18.409us	9.2170us	58.886us	cupy_sum							
6.5	% 7.4023ms	1002	7.3870us	1.1200 us	17.282 us	cupy_true_divide							
API calls: 100.0	% 170.54 ms	4008	42.549us	31.648 us	433.38us	cuLaunchKernel							
==31551== Range "inner_loop_custom_kernel"													
Type Time(5) Time	Calls	Avg	Min	Max	Name							
Range: 100.00	% 109.58ms	1002	109.36 us	91.296us	1.5531ms								
GAbeactovptcestom100.00	% 23.676ms	1002	23.628us	18.721 us	27.683us	average_power							
API calls: 100.00	% 32.936ms	1002	32.869us	30.208us	104.70 us	cuLaunchKernel							

Profiler immediately shows each cupy kernel and the overhead of kernel launch. Note the lack of any memory copying calls in either case!

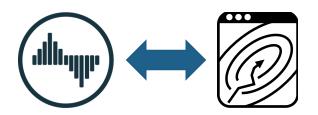
Example: Performing Inference

$\bigcirc \bigcirc \bigcirc \bigcirc$

import tensorrt

```
# Input and output buffers created using numba.cuda.mapped_array().
# Input is samples from radio, output is inference results from the network.
```

Signal Processing Recap



Python signal processing code can be shared between training and inference pipelines



Rich open-source libraries exist today to make this easy on the GPU

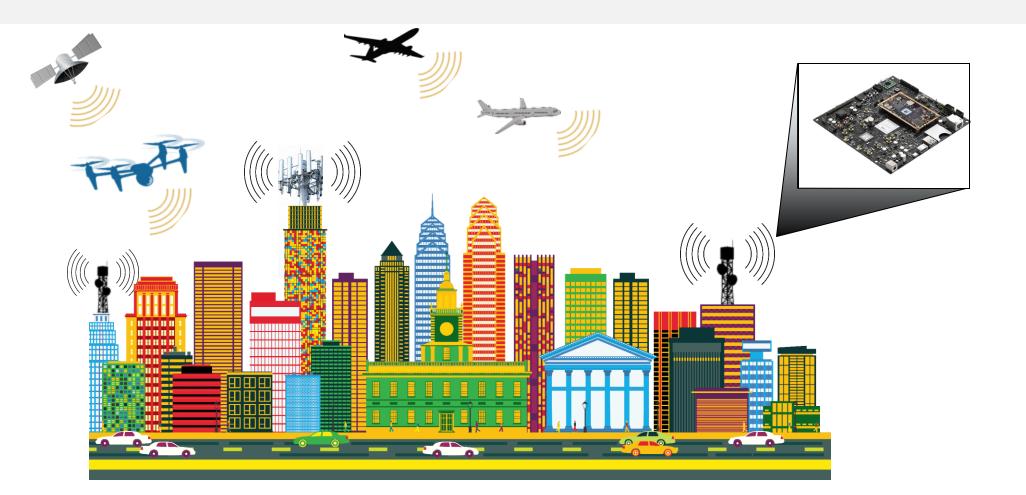


Profiling support is mature and will help you optimize



Algorithms can be easily wrapped in GNU Radio blocks or python to integrate deep learning with a larger signal processing system

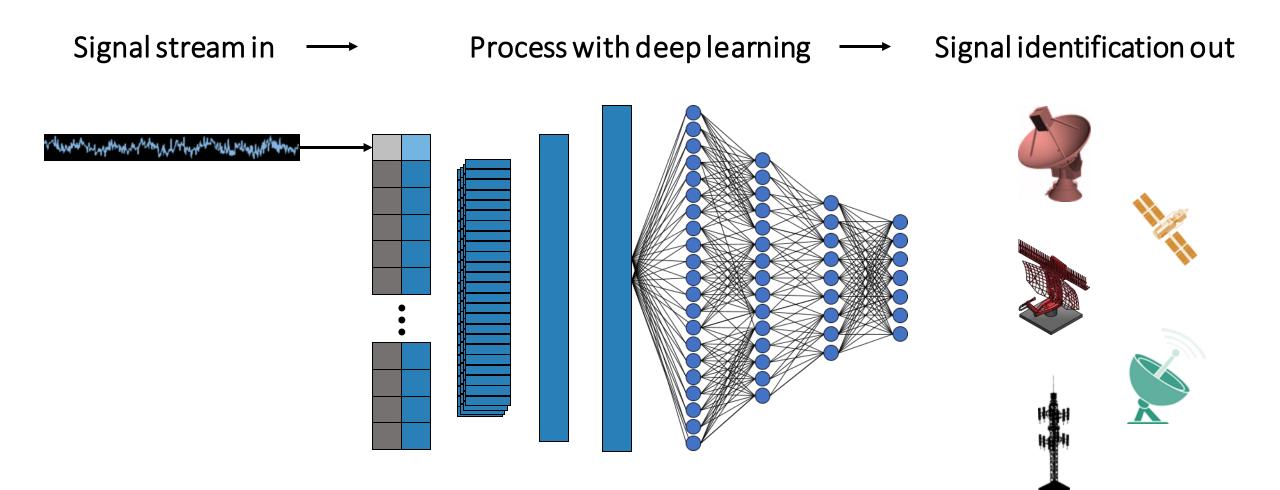
Deep Learning Wireless Deployment Scenario



Goal: Detect and classify signals in congested environment using AIR-T

Radar Signal Detector Model

Example Classifier



Radar Signal Detector Model: Transmitted Signals

Radar Waveform	Nothin	Interfe	Sunein Ce	Ground	Ground	M7, "U(15M2)	Airbor	Airborn Medipari	Ground High PACI	Nautic Soft Con	Nautics, Short Par	Nautican Rang	Ground Rand	Ground Michael	Ground Country	~ (NLFN)~
Linear Pulse			X	X	X					X	X	X				
Non-Linear Pulse													X	X	X	
Phase Coded Pulse									Х							
Pulsed Doppler						X	X	Х								

Technique demonstration shown with nominal radar signals

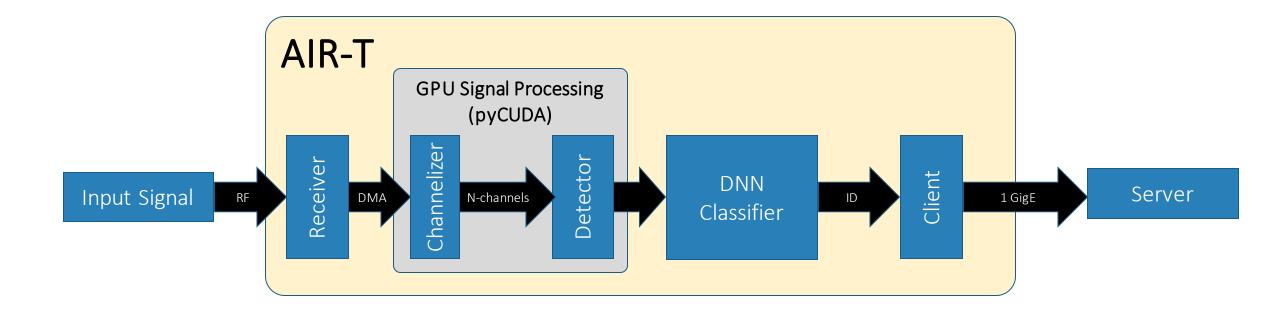
• Method applicable to communications, cellular, and other RF protocols

Real Time Deep Neural Network (DNN) Classifier

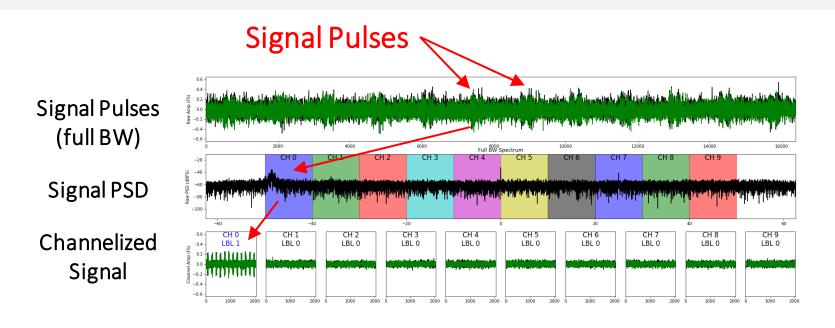
Monitors 125 MHz of instantaneous bandwidth

- Trained on 10,000+ signal segments
- Hardened through channel models and analog distortions

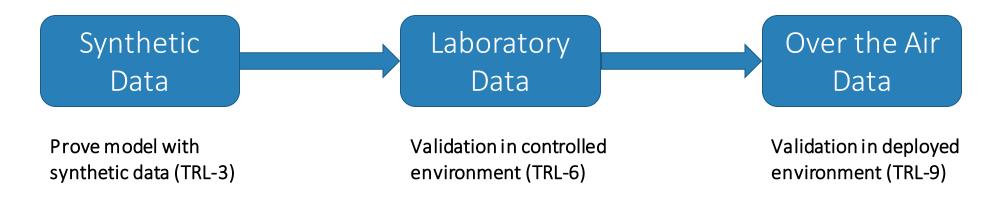
- 125 MSPS GPU channelizer
- Modular design



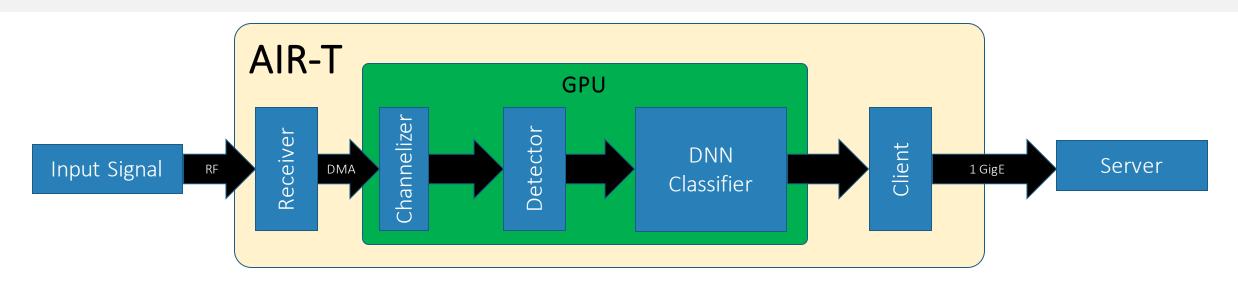
Pre-processing Overview and Methodology



- Significantly improves signal classification performance
 - Increases SNR and PCC
 - Signal isolation
- Provide separate path for negative SNR (signal less likely to be present) cases

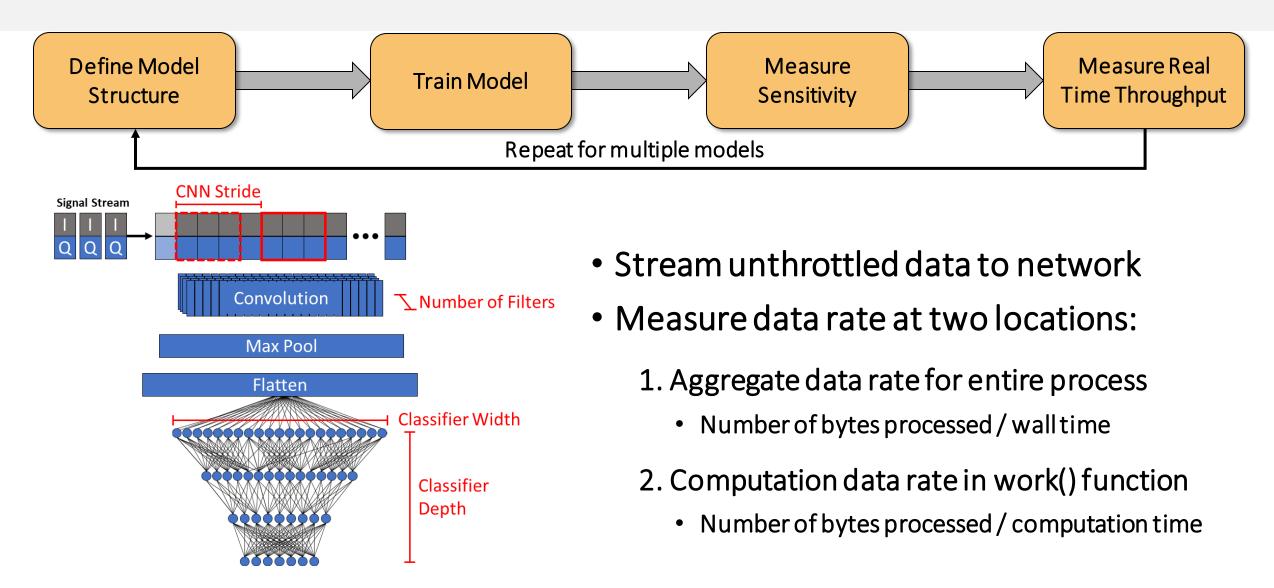


Processing Utilization on AIR-T

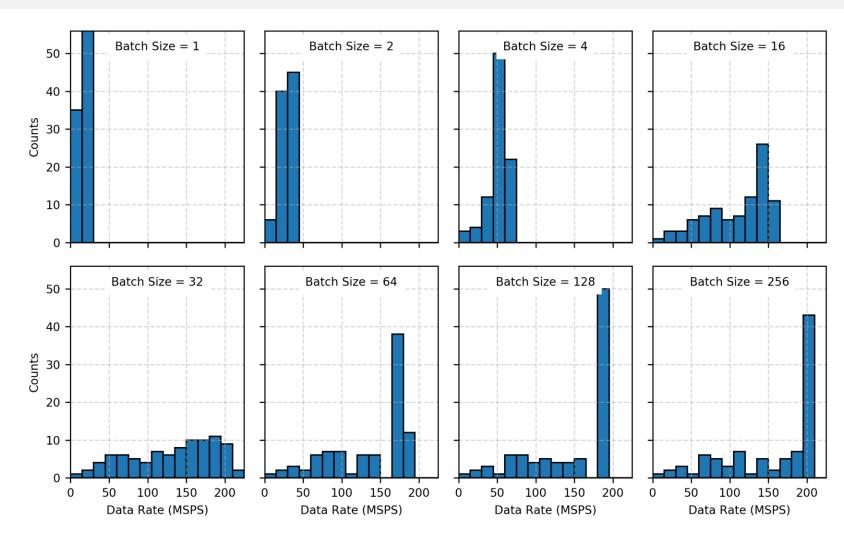


- CPU utilization:
 - 40% of one ARM core for processing
 - 30% of one ARM core for network I/O, data logging, and other management tasks
- GPU utilization:
 - 85% includes both signal processing and inference tasks

Performance Benchmarking Test Setup



Data Rate Benchmark for AIR-T (Jetson TX2)

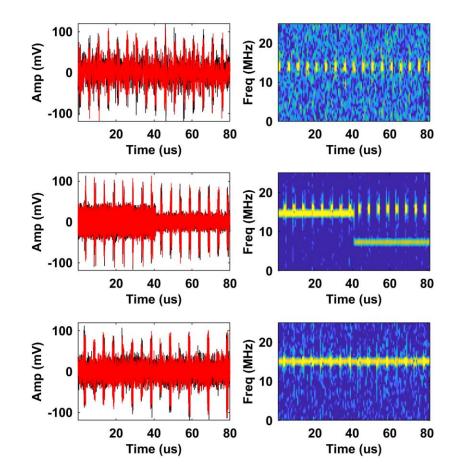


- Tested 91 different CNN classifier models with 8 batch sizes
 - 728 models tested
- Able to achieve 200 MSPS (real samples) with AIR-T

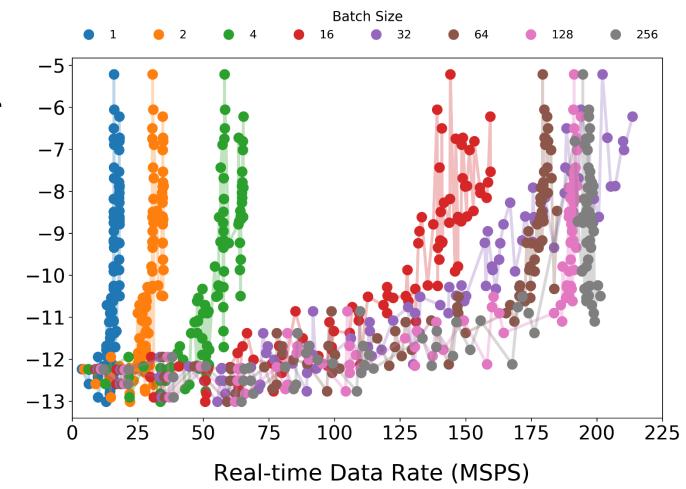




Model Accuracy Benchmarks

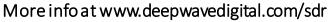






Summary

- Deep learning within signal processing is emerging
- High bandwidth requirements driving edge solutions
 - Embedded GPUs now suitable for signal processing
- Deepwave developed AIR-T
 - Edge-compute inference engine with MIMO transceiver
 - FPGA, CPU, GPU
- Open-source Python ecosystem for deep learning with signals is improving daily: give it a try!
- Benchmarking demonstrates real-time signal classification inference at rates approaching 200 MSPS with AIR-T









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